	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	BRS	37497	(((frequency adj1 synthesi$4) or pll or (phase adj1 locked adj1 loop)) and oscillator	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 10:48			0
2	BRS	8015	(((frequency adj1 synthesi$4) or pll or (phase adj1 locked adj1 loop)) and oscillator and (parameter$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 10:49			0
3	BRS	402226	(((frequency adj1 synthesi$4) or pll or (phase adj1 locked adj1 loop)) and oscillator and (parameter$1) and (integrated adj1 circuit) or ic or on$1chip or (on adj1 chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 10:51			0
4	BRS	3402	((((frequency adj1 synthesi$4) or pll or (phase adj1 locked adj1 loop)) and oscillator and (parameter$1)) and (((frequency adj1 synthesi$4) or pll or (phase adj1 locked adj1 loop)) and oscillator and (parameter$1) and (integrated adj1 circuit) or ic or on$1chip or (on adj1 chip))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 11:02			0
5	BRS	4	re$1center$3 near (pll or (phase adj1 lock$2 adj1 loop))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 10:53			0
6	BRS	2699	(((((frequency adj1 synthesi$4) or pll or (phase adj1 locked adj1 loop)) and oscillator and (parameter$1)) and (((frequency adj1 synthesi$4) or pll or (phase adj1 locked adj1 loop)) and oscillator and (parameter$1) and (integrated adj1 circuit) or ic or on$1chip or (on adj1 chip))) and (control and operating)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 11:03			0
7	BRS	1	(((((frequency adj1 synthesi$4) or pll or (phase adj1 locked adj1 loop)) and oscillator and (parameter$1)) and (((frequency adj1 synthesi$4) or pll or (phase adj1 locked adj1 loop)) and oscillator and (parameter$1) and (integrated adj1 circuit) or ic or on$1chip or (on adj1 chip))) and ((control and operating) near parameter)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 11:03			0
8	BRS	57	(((((frequency adj1 synthesi$4) or pll or (phase adj1 locked adj1 loop)) and oscillator and (parameter$1)) and (((frequency adj1 synthesi$4) or pll or (phase adj1 locked adj1 loop)) and oscillator and (parameter$1) and (integrated adj1 circuit) or ic or on$1chip or (on adj1 chip))) and ((control and operating) near parameters)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 11:06			0
9	BRS	5900	control$4 near (pll or (phase adj1 lock$2 adj1 loop))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 11:07			0
10	BRS	360	(control$4 near (pll or (phase adj1 lock$2 adj1 loop))) and (state adj1 machine$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 11:30			0

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
11	BRS	2213	(control$4 near (pll or (phase adj1 lock$2 adj1 loop))) and ((state adj1 machine$1) or controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 11:58			0
12	BRS	94	((control$4 near (pll or (phase adj1 lock$2 adj1 loop))) and ((state adj1 machine$1) or controller)) and ((operat$4 adj1 parameter) or pvt or temperature or voltage or process) near control$4 near loop	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 11:35			0
13	BRS	94	((control$4 near (pll or (phase adj1 lock$2 adj1 loop))) and ((state adj1 machine$1) or controller)) and (((operat$4 adj1 parameter) or pvt or temperature or voltage or process) near control$4 near loop)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 11:35			0
14	BRS	280	((control$4 near (pll or (phase adj1 lock$2 adj1 loop))) and ((state adj1 machine$1) or controller)) and (loop adj1 filter) same compar$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 11:47			0
15	BRS	1645	((control$4 near (pll or (phase adj1 lock$2 adj1 loop))) and ((state adj1 machine$1) or controller)) and (calibrat$4 or tun$4 or normal$4 or center$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 11:59			0
16	BRS	260	(331/$$$.ccls. or 327/$$$.ccls.) and (((control$4 near (pll or (phase adj1 lock$2 adj1 loop))) and ((state adj1 machine$1) or controller)) and (calibrat$4 or tun$4 or normal$4 or center$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 12:01			0
17	BRS	140	((331/$$$.ccls. or 327/$$$.ccls.) and (((control$4 near (pll or (phase adj1 lock$2 adj1 loop))) and ((state adj1 machine$1) or controller)) and (calibrat$4 or tun$4 or normal$4 or center$3))) and (comparision or comparator$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 12:03			0
18	BRS	1	(((331/$$$.ccls. or 327/$$$.ccls.) and (((control$4 near (pll or (phase adj1 lock$2 adj1 loop))) and ((state adj1 machine$1) or controller)) and (calibrat$4 or tun$4 or normal$4 or center$3))) and (comparision or comparator$1)) and (pvt same voltage same temperature same process)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 12:06			0
19	BRS	104	(((331/$$$.ccls. or 327/$$$.ccls.) and (((control$4 near (pll or (phase adj1 lock$2 adj1 loop))) and ((state adj1 machine$1) or controller)) and (calibrat$4 or tun$4 or normal$4 or center$3))) and (comparision or comparator$1)) and (variation$1 or (pvt same voltage same temperature same process) or parameter$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 13:26			0
20	BRS	138	(((331/$$$.ccls. or 327/$$$.ccls.) and (((control$4 near (pll or (phase adj1 lock$2 adj1 loop))) and ((state adj1 machine$1) or controller)) and (calibrat$4 or tun$4 or normal$4 or center$3))) and (comparision or comparator$1)) and (variation$1 or pvt or voltage or temperature or process or parameter$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 13:33			0

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
21	BRS	45	pll same compensa$5 same temperature same voltage same process	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 13:34			0